	Hits	Search Text
109	3	LCD and (((multiplex$4) adj3 (set group)) and ((data signal column address)) with ((odd even) near5 driv$5))
110	7	LCD and (((multiplex$4) with (group)) and ((data signal column address)) with ((odd even) near5 driv$5))
111	6	LCD with (column adj4 ((odd even) near5 driv$5))
112	0	LCD same (column adj ((odd even) near driv$5))
113	5	LCD and (column adj ((odd even) near driv$5))
114	8	LCD same (column near2 ((odd even) near2 driv$5))
115	0	"6166715.pn" and substrate
116	0	"6166715.pn" and layout
117	0	"6166715".pn. and substrate
118	1	"6166715".pn. and layout
119	838	(345/98).CCLS.
120	365	(345/99).CCLS.
121	793	(345/100).CCLS.
122	471	(345/92).CCLS.
123	210	(345/103).CCLS.
124	824	(345/204).CCLS.
125	245	(345/205).CCLS.
126	188	(345/206).CCLS.
127	205	((data drain signal) adj3 process$4 adj2 (block circuit)) with (substrate) and LCD
128	9	(order with ((data drain signal) adj3 process$4 adj2 (block circuit))) with (substrate) and LCD
129	0	((first and last) with ((data) adj3 process$4 adj2 (block circuit))) with (substrate) and LCD
130	0	((order) with ((data) adj3 process$4 adj2 (block circuit))) with (substrate) and LCD
131	0	((adjacent) with ((data) adj3 process$4 adj2 (block circuit))) with (substrate) and LCD
132	0	((first and last) with ((data) adj3 process$4 adj2 (block circuit))) same (substrate) and LCD
133	0	((order) with ((data) adj3 process$4 adj2 (block circuit))) same (substrate) and LCD

	DBs
109	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
110	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
111	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
112	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
113	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
114	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
115	USPAT
116	USPAT
117	USPAT
118	USPAT
119	USPAT; USOCR
120	USPAT; USOCR
121	USPAT; USOCR
122	USPAT; USOCR
123	USPAT; USOCR
124	USPAT; USOCR
125	USPAT; USOCR
126	USPAT; USOCR
127	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
128	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
129	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
130	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
131	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
132	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
133	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

	Hits	Search Text
134	0	((first and last) with ((data) adj3 process$4 adj2 (block circuit))) same (substrate)
135	6	((order) with ((data) adj3 process$4 adj2 (block circuit))) same (substrate)
136	0	((first and last) same ((data) adj3 process$4 adj2 (block circuit))) same (substrate)
137	9	((first and last) same ((data) near4 (block circuit))) same (substrate) and LCD
138	22	((first and last) same ((data) near4 (block circuit))) same (substrate)
139	1150	((first and last) same ((block circuit))) same (substrate)
140	310	((order) same ((block circuit))) same (substrate) same LCD
141	9	((first and last) same ((block circuit))) same (substrate) same LCD
142	758	((first same last) same ((block circuit))) same (substrate)
143	8	((first same last) same ((block circuit))) same (substrate) same LCD
144	310	((order) same ((block circuit))) same (substrate) same LCD
145	30	((order) adj4 ((block circuit))) same (substrate) same LCD
146	1	((first same last) adj4 ((block circuit))) same (substrate) same LCD
147	1	((first same last) near5 ((block circuit))) same (substrate) same LCD
148	65	((first same last) near5 ((block circuit))) same (substrate)
149	54	((order) near4 ((block circuit))) same (substrate) same LCD
150	24	S179 not S175
151	11	(((first same last) adj3 conductor) same ((block circuit))) same (substrate)
152	12	(((first and last) adj3 conductor) same ((block circuit))) same (substrate)
153	17	(((first and last) near4 conductor) same ((block circuit))) same (substrate)

	DBs
134	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
135	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
136	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
137	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
138	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
139	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
140	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
141	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
142	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
143	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
144	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
145	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
146	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
147	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
148	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
149	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
150	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
151	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
152	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
153	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

	Hits	Search Text
154	13	(((first and last) adj3 conductor) same ((group block circuit))) same (PCB substrate)
155	25	(((first and last) adj3 (line conductor block)) same ((group circuit))) same (PCB substrate)
156	102	(((first and last) adj3 (line conductor block))) same (PCB substrate)
157	19	(((first and last) adj3 (line conductor block))) same (PCB substrate) and LCD
158	40	((data drain signal) adj3 process$4 adj2 (block circuit)) with (substrate and LCD)
159	40	((data drain signal) adj3 process$4 adj2 (block group circuit)) with (substrate and LCD)
160	63	((data drain signal) adj3 process$4 adj2 (block group circuit)) with (substrate same order)
161	63	((data drain signal) adj3 process$4 adj2 (block group circuit modul$4)) with (substrate same order)
162	196	((data drain signal) adj3 process$4 adj2 (block group circuit modul$4)) same (substrate same order)
163	102	((data drain signal) adj3 process$4 adj2 (block group circuit modul$4)) same (substrate with order)
164	34	((data drain signal) adj3 process$4 adj2 (block group circuit modul$4)) same (substrate same layout)
165	38	(DAC D/A (digital adj5 analog adj2 convert$3)) same (substrate same layout)
166	2508	(345/48,50,51,52,55-60,75,82,564,568,572,573,574).CCLS.
167	0	((data drain signal) adj3 process$4 adj2 (block group circuit modul$4)) same (substrate same order) and S211
168	11	((data drain signal) adj3 process$4 adj2 (block group circuit modul$4)) and (substrate same order) and S211
169	333	LCD and ((set group) with (signal column address)) with (substrate)
170	192	LCD and ((set group) with (data signal column address drain)) and (substrate) and ((first last) near4 signal adj line)
171	55	LCD and ((set group) with (data signal column address drain)) same (substrate) and ((first last) near4 signal adj line)

	DBs
154	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
155	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
156	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
157	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
158	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
159	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
160	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
161	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
162	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
163	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
164	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
165	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
166	USPAT
167	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
168	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
169	USPAT
170	USPAT
171	USPAT

	Hits	Search Text
172	57	LCD and ((set group) with (data signal column address drain)) same (substrate) and ((first last) near4 signal adj line)
173	8	LCD and ((set group) with (data signal column address drain)) and (substrate) and ((first and last) near4 signal adj line)
174	9	LCD and ((set module block group) with (data signal column address drain)) and (substrate) and ((first and last) near4 signal adj line)
175	1	S225 not S224
176	9	LCD and ((set module block group) same (data signal column address drain)) and (substrate) and ((first and last) near4 signal adj line)
177	10	LCD and ((set module block group) and (data signal column address drain)) and (substrate) and ((first and last) near4 signal adj line)
178	14	LCD and ((set module block group) and (data signal column address drain)) and (substrate) and ((first and last) near4 signal adj line)
179	0	((first and last) same ((data) adj3 process$4 adj2 (block circuit))) with (substrate) and LCD
180	0	((first and last) same ((data) adj3 process$4 adj2 (block circuit))) and (substrate) and LCD.ab.
181	1	((first and last) and ((data) adj3 process$4 adj2 (block circuit))) and (substrate) and LCD.ab.
182	2	(first and last) same (address near3 conductor$4) same (substrate) and LCD
183	35	((first and last) and ((data) adj3 process$4 adj2 (block circuit))) and (substrate) .ab.
184	20	((first and last) same ((data) adj3 process$4 adj2 (block circuit))) and (substrate) and LCD
185	63	((data drain signal) adj3 process$4 adj2 (block group circuit modul$4)) with (substrate same order)

	DBs
172	USPAT; EPO; JPO; DERWENT; IBM_TDB
173	USPAT; EPO; JPO; DERWENT; IBM_TDB
174	USPAT; EPO; JPO; DERWENT; IBM_TDB
175	USPAT; EPO; JPO; DERWENT; IBM_TDB
176	USPAT; EPO; JPO; DERWENT; IBM_TDB
177	USPAT; EPO; JPO; DERWENT; IBM_TDB
178	US-PGPUB
179	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
180	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
181	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
182	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
183	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
184	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
185	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB